## AN OVERVIEW OF PYTHONPDEVS

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## MS4 Me DEVS-Suite X-S-Y CD++ PowerDEVS VLE Adevs



```
from pypdevs.DEVS import *
class TrafficLightAutonomous(AtomicDEVS):
    def __init__(self):
        AtomicDEVS.__init__(self, "Light")
        self.state = "green"
        self.observe = self.addOutPort("observer")
        self.interrupt = self.addlnPort("interrupt")
    def intTransition(self):
        state = self.state
        return {"red": "green",
            "yellow": "red",
                "green": "yellow"}[state]
    def timeAdvance(self):
        state = self.state
        return {"red": 60,
            "yellow": 3,
            "green": 57}[state]
```

```
def extTransition(self, inputs):
    inp =
inputs[self.interrupt][0]
    if inp == "manual":
        return "manual"
    elif inp == "auto":
        if self.state == "manual":
        return "red"
def outputFnc(self):
    state = self.state
    if state == "red":
        v = "green"
    elif state == "yellow":
        v = "red"
    elif state == "green":
        v = "yellow"
    return {self.observe: [v]}
```

Time




Time

| Execution <br> modes | As-fast-as- <br> possible | Realtime |
| :---: | :---: | :---: |
| Sequential | $\boldsymbol{\checkmark}$ | $\boldsymbol{\checkmark}$ |
| Parallel | $\boldsymbol{x}$ | $\boldsymbol{x}$ |
| Distributed | $\boldsymbol{y}$ | $\boldsymbol{x}$ |


| Features | Sequential <br> AFAP | Sequential <br> RT | Distributed <br> AFAP |
| :--- | :---: | :---: | :---: |
| Classic DEVS | $\checkmark$ | $\checkmark$ | $\boldsymbol{x}$ |



| Features | Sequential <br> AFAP | Sequential <br> RT | Distributed <br> AFAP |
| :--- | :---: | :---: | :---: |
| Classic DEVS | $\checkmark$ | $\checkmark$ | $\boldsymbol{\chi}$ |
| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |



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| Classic DEVS | $\checkmark$ | $\checkmark$ | $\boldsymbol{\chi}$ |
| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Dynamic Structure | $\checkmark$ | $\checkmark$ | $\boldsymbol{x}$ |



| Features | Sequential <br> AFAP | Sequential <br> RT | Distributed <br> AFAP |
| :--- | :---: | :---: | :---: |
| Classic DEVS | $\checkmark$ | $\checkmark$ | $\mathbf{x}$ |
| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Dynamic Structure | $\checkmark$ | $\checkmark$ | $\boldsymbol{x}$ |
| Tracing | $\checkmark$ | $\checkmark$ | $\checkmark$ |

## 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5



07:37:21 sleeper-laptop kernel: 07:38:34 sleeper-laptop kernel: 07:38:38 sleeper-laptop kernel: 07:40:25 sleeper-laptop kernel: 07:41:14 sleeper-laptop kernel: 07:42:57 sleeper-laptop kernel: 07:43:22 sleeper-laptop kernel: 07:43:22 sleeper-laptop kernel: $07: 43: 22$ sleeper-laptop kernel: 07:43:24 sleeper-laptop kernel: 07:43:27 sleeper-laptop kernel: 07:43:27 sleeper-laptop kernel: 07:43:27 sleeper-laptop kernel: 07:43:27 sleeper-laptop kernel: $07: 43: 2$ 07:43:27 sleeper-laptop kernel: 07:44:15 sleeper-laptop kernel: 07:45:35 sleeper-laptop kernel: 07:45:41 sleeper-laptop kernel: 07:45:44 sleeper-laptop kernel: 07:45:45 sleeper-laptop kernel: 07:45:52 sleeper-laptop kernel: 07:48:09 sleeper-laptop kernel: 07:48:13 sleeper-laptop kernel: 07:48:28 sleeper-laptop kernel: 07:49:03 sleeper-laptop kernel: 07:49:05 sleeper-laptop kernel: 07:49:16 sleeper-laptop kernel: 07:49:24 sleeper-laptop kernel: 07:49:32 sleeper-laptop kernel: 07:49:46 sleeper-laptop kernel: 07:50:04 sleeper-laptop kernel: 07:50:32 sleeper-laptop kernel:
[48289.132677] Inbound IN=wlan0 OU
[48378.797582] Inbound IN=wlane OU [48389.619090] Inbound $I N=w l a n 90 U$ [48451.842293] Inbound $\mathrm{IN}=$ wlane OU [48455.594017] Inbound $\mathrm{IN}=$ =wlan0 OU [48562.503720] Inbound IN=wlane OU [48611.958176] Inbound $\mathrm{IN}=$ wlan0 OU [48714.901000] Inbound $\mathrm{IN}=w$ lan0 0 L [48739.104070] usb 1-3: new high s [48739.376413] usb 1-3: configurat [48739.376805] scsi5 : SCSI emulat [48741.827216] Inbound $\mathrm{IN}=$ wlan0 OU [48744.411585] scsi 5:0:0:0: Direc [48744.412236] sd 5:0:0:0: Attache [48744.414558] sd 5:0:0:0: [sdc] [48744.416297] sd 5:0:0:0: [sdc] $h$ [48744.418185] sdc: sdc1
[48744.464210] sd 5:0:0:0: [sdc] [48792.714707] Inbound $\mathrm{IN}=$ wlane OU [48872.485324] Inbound $\mathrm{IN}=$ wlan0 0 L [48878.300045] Inbound $\mathrm{IN}=w$ lane 0 O [48881.351217] Inbound IN=wlan0 OU [48882.303692] Inbound $\mathrm{IN}=w$ lane 0 O [48889.430725] Inbound $\mathrm{IN}=$ wlan0 [49026.184589] Inbound IN=wlane OU [49030.735783] Inbound $\mathrm{IN}=w$ lan 0 OU [49046.015773] Inbound $I N=w l a n \theta$ OU [49080.984064] Inbound $\mathrm{IN}=w$ lane 0 O [49082.772667] Inbound IN=wlan0 OU [49093.891441] Inbound $\mathrm{IN}=$ wlan0 OU 49101.397107] Inbound $\mathrm{IN}=$ =wlan9 OU [49109.971138] Inbound $I N=w l a n 0$ [49123.643806] Inbound $\mathrm{IN}=$ wlan0 OU [49141.095465] Inbound $\mathrm{IN}=w$ lane OL [49169.224255] Inbound $\mathrm{IN}=$ wlane OU [49184.219329] Inbound $\mathrm{IN}=$ wlan0 OU [49301.640721] Inbound $\mathrm{IN}=w$ lane 0 O 49314.942914] Inbound IN=wlane OU 385.374850] Inbound $\mathrm{IN}=w \operatorname{lan} 0$ 493868985711 Inbound TM= 1ane 01

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| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Dynamic Structure | $\checkmark$ | $\checkmark$ | $\boldsymbol{x}$ |
| Tracing | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Checkpointing | $\checkmark$ | $\boldsymbol{x}$ | $\checkmark$ |



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| Tracing | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Checkpointing | $\checkmark$ | $\mathbf{x}$ | $\boldsymbol{\checkmark}$ |
| Nested Simulation | $\boldsymbol{V}$ | $\boldsymbol{V}$ | $\mathbf{x}$ |



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| Dynamic Structure | $\checkmark$ | $\checkmark$ | $x$ |
| Tracing | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Checkpointing | $\checkmark$ | $x$ | $\checkmark$ |
| Nested Simulation | $\checkmark$ | $\checkmark$ | $x$ |
| Termination Cond. | $\checkmark$ | $\checkmark$ | $\checkmark$ |


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| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Dynamic Structure | $\checkmark$ | $\checkmark$ | $\mathbf{x}$ |
| Tracing | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Checkpointing | $\checkmark$ | $\mathbf{x}$ | $\checkmark$ |
| Nested Simulation | $\checkmark$ | $\checkmark$ | $\mathbf{x}$ |
| Termination Cond. | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Livelock Detection | $\checkmark$ | $\checkmark$ | $\checkmark$ |



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| Parallel DEVS | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Dynamic Structure | $\checkmark$ | $\checkmark$ | $\boldsymbol{~}$ |


| Performance | Sequential <br> AFAP | Sequential <br> RT | Distributed <br> AFAP |
| :--- | :---: | :---: | :---: |
| Direct Connect | $\checkmark$ | $\checkmark$ | $\checkmark$ |


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| Single Loop | $\checkmark$ | $\checkmark$ | $\checkmark$ |


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| Direct Connect | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Single Loop | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Termination Time | $\checkmark$ | $\checkmark$ | $\checkmark$ |



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| Single Loop | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Termination Time | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Scheduling Hints | $\checkmark$ | $\checkmark$ | $\checkmark$ |



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| Single Loop | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Termination Time | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Scheduling Hints | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Migration Hints | $\boldsymbol{x}$ | $\mathbf{x}$ | $\checkmark$ |



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| Migration Hints | $\boldsymbol{x}$ | $\mathbf{x}$ | $\checkmark$ |
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| Scheduling Hints | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Migration Hints | $\boldsymbol{x}$ | $\boldsymbol{x}$ | $\checkmark$ |
| Allocation Hints | $\boldsymbol{x}$ | $\boldsymbol{x}$ | $\checkmark$ |
| Memoization Hints | $\boldsymbol{x}$ | $\boldsymbol{x}$ | $\checkmark$ |

## $?$




